Notice of References Cited Application/Control No. 10/621,908 Examiner Benjamin P. Geib Applicant(s)/Patent Under Reexamination DHONG ET AL. Art Unit Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			
	С	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Ι	US-			
	_	US-			
	7	US-			
	К	US-	40		
	اد	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	Q					
	R					
	S					
	Т					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	"AltiVec Technology Programming Environments Manual"; Feb. 2002; Chapter 1 (pages 1-12) and Chapter 6 (pages 1-8, 170)					
	٧	"AltiVec: Bringing Vector Technology to the PowerPC Processor Family"; Tyler et al.; 1999; IEEE					
	W						
	x						

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.